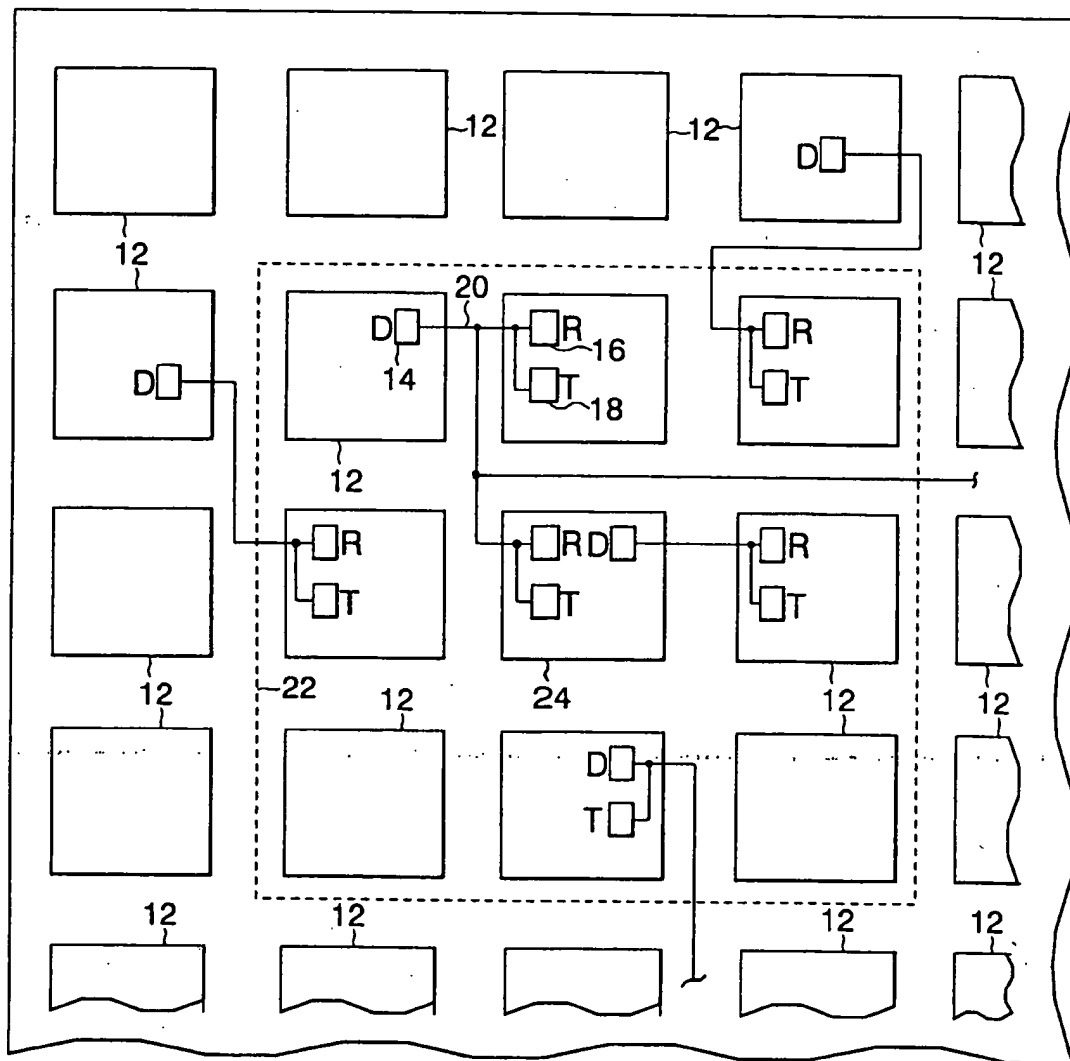


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REVIEWED
OK
FR
2-26-05



(Prior Art)

Figure 1

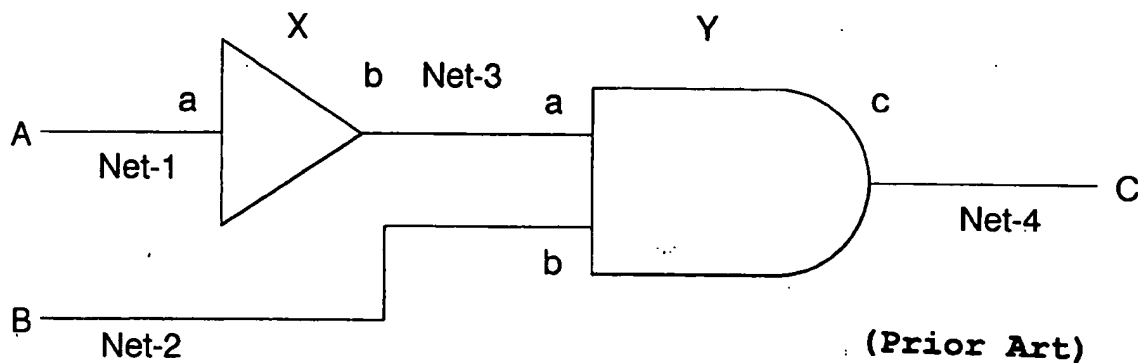
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REVIEWED

OK

gfa

2-26-05



EXAMPLE OF LOGIC CIRCUIT

Figure 2

```

Port:
  INPUT : A, B:
  OUTPUT: C
EndPort
Net:
  Net-1: pA, Xpa;
  Net-2: pB, Ypb;
  Net-3: Xpb, Ypa;
  Net-4: pC, Ypc;
EndNet;
    
```

(Prior Art)

EXAMPLE OF NET LIST

Figure 3

Reviewed "OK"

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(RAH) BUR92001,0207US1

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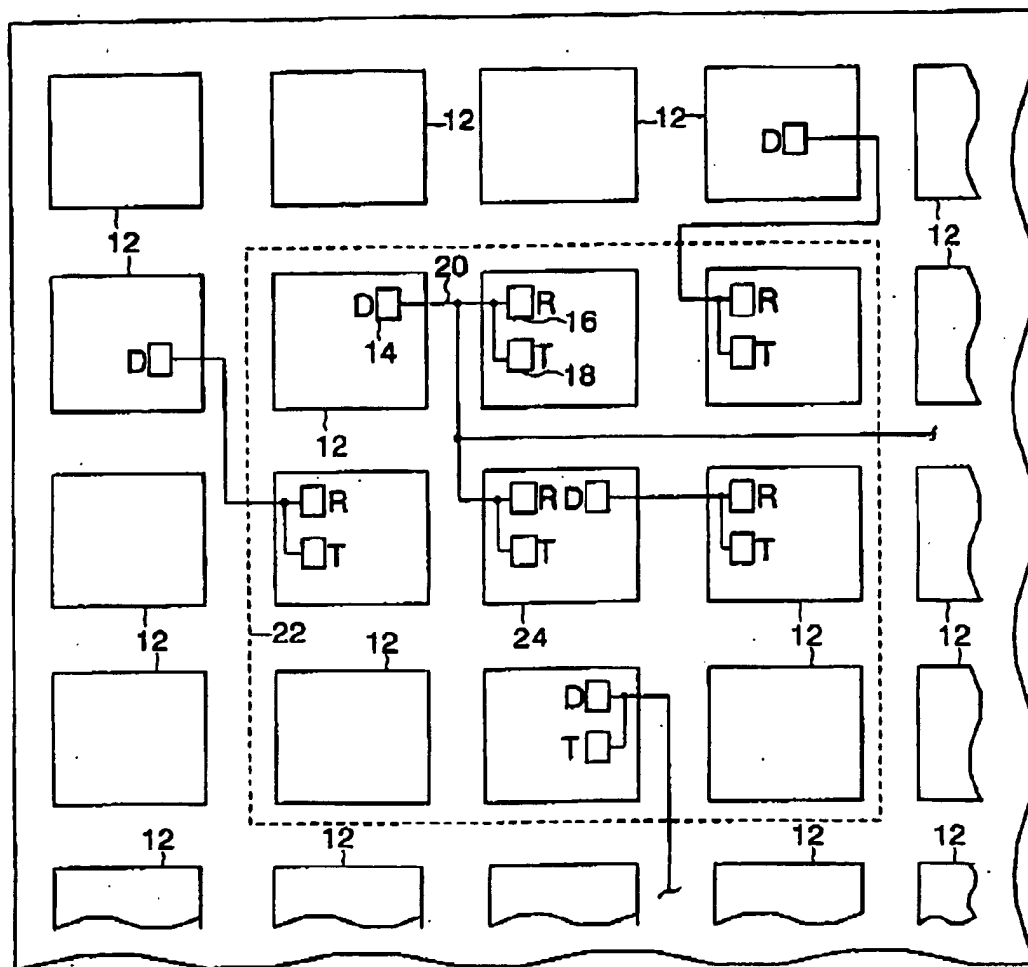


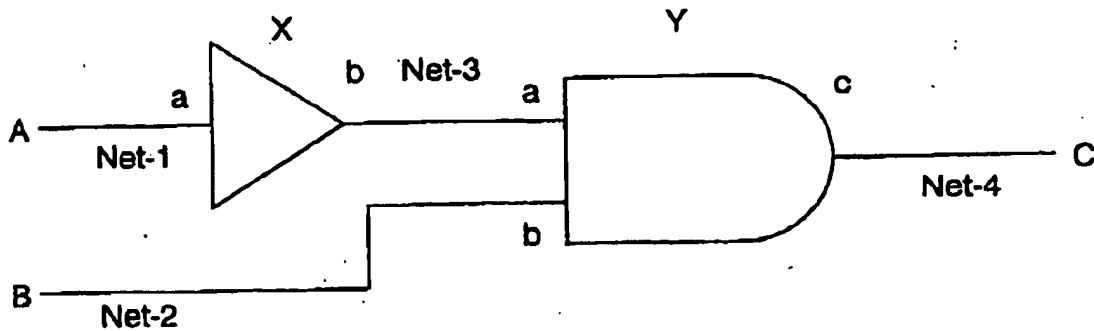
Figure 1 (Prior Art)

Reviewed "OK"

JSK
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EXAMPLE OF LOGIC CIRCUIT

Figure 2 (Prior Art)

Port:
 INPUT : A, B;
 OUTPUT: C
 EndPort
 Net:
 Net-1: pA, Xpa;
 Net-2: pB, Ypb;
 Net-3: Xpb, Ypa;
 Net-4: pC, Ypc;
 EndNet;

EXAMPLE OF NET LIST

Figure 3 (Prior Art)